IN THE CLAIMS:

1. and 2. (Canceled)

3. (Currently Amended) ~~A display device according to claim 2, further comprising~~ A display device comprising a display module which determines a plurality of n (n is an integer equal to or more than 2) pieces of pixels as belonging to one block unit, selects the plurality of pixels in each block unit at the same time and displays a picture image by adding one or a plurality of specific patterns each having different spatial frequencies of each block unit; a display control unit which controls the display module; a picture image signal generation unit which generates picture image signals; and a computing circuit which generates the specific patterns each having different spatial frequencies while weighting the same based on the picture image signals for every block unit;

wherein the computing circuit is a means for generating n pieces of specific patterns each having different spatial frequencies which are weighted based on the picture image signals for every block unit, and the display module is a means for displaying a picture image by adding Np (which is an integer smaller than n) pieces of the specific patterns; and,

a compression rate regulation unit which modifies the number of pieces Np of the specific patterns to be applied.

4. (Currently Amended) ~~A display device according to claim 2, further comprising~~ A display device comprising a display module which determines a plurality of n (n is an integer equal to or more than 2) pieces of pixels as belonging to one block unit, selects the plurality of pixels in each block unit at the same time and displays a picture image by adding one or a plurality of specific patterns each having different spatial frequencies of each block unit; a display control unit which controls the display module; a picture image signal generation unit which generates picture image signals; and a computing circuit which generates the specific patterns each having different spatial frequencies while weighting the same based on the picture image signals for every block unit;

wherein the computing circuit is a means for generating n pieces of specific patterns each having different spatial frequencies which are weighted based on the picture image signals for every block unit, and the display module is a means for displaying a picture image by adding Np (which is an integer smaller than n) pieces of the specific patterns; and,

a high compression rate computing circuit which modifies the number of the specific patterns to be added for every block unit.

5. (Currently Amended) ~~A display device according to claim 1,~~ A display device comprising a display module which determines a plurality of n (n is an integer equal to or more than 2) pieces of pixels as belonging to one block unit, selects the plurality of pixels in each block unit at the same time and displays a picture image by adding one or a plurality of specific patterns each having different spatial frequencies

of each block unit; a display control unit which controls the display module; a picture image signal generation unit which generates picture image signals; and a computing circuit which generates the specific patterns each having different spatial frequencies while weighting the same based on the picture image signals for every block unit;

wherein the display module includes a panel in which the pixels are arranged in a matrix shape, a signal driver, a scan driver and opposing signal driver; signal lines connected to the signal driver; scan lines connected to the scan driver; and opposing signal lines connected to the opposing signal driver; each of the pixels includes a signal electrode, opposing signal electrode and a switch element, the signal electrode is connected to one of the signal lines via the switch element, the opposing signal electrode is connected to one of the opposing signal lines, a first potential is applied to the signal electrodes provided for the pixels on a same line included in a same block unit, a second potential is applied to the opposing signal electrodes provided for the pixels on a same row included in the same block unit, a certain specific pattern is formed by the first and second potentials for the same block unit concerned and one of the common opposing signal lines is connected to the opposing signal electrodes provided for the pixels on the same line.

6. (Currently Amended) ~~A display device according to claim 2,~~ A display device comprising a display module which determines a plurality of n (n is an integer equal to or more than 2) pieces of pixels as belonging to one block unit, selects the plurality of pixels in each block unit at the same time and displays a picture image by

adding one or a plurality of specific patterns each having different spatial frequencies of each block unit; a display control unit which controls the display module; a picture image signal generation unit which generates picture image signals; and a computing circuit which generates the specific patterns each having different spatial frequencies while weighting the same based on the picture image signals for every block unit;

wherein the computing circuit is a means for generating n pieces of specific patterns each having different spatial frequencies which are weighted based on the picture image signals for every block unit, and the display module is a means for displaying a picture image by adding Np (which is an integer smaller than n) pieces of the specific patterns; and,

wherein the display module includes a panel in which the pixels are arranged in a matrix shape, a signal driver, a scan driver and opposing signal driver; signal lines connected to the signal driver; scan lines connected to the scan driver; and opposing signal lines connected to the opposing signal driver; each of the pixels includes a signal electrode, opposing signal electrode and a switch element, the signal electrode is connected to one of the signal lines via the switch element, the opposing signal electrode is connected to one of the opposing signal lines, a first potential is applied to the signal electrodes provided for the pixels on a same line included in a same block unit, a second potential is applied to the opposing signal electrodes provided for the pixels on a same row included in the same block unit, a certain specific pattern is formed by the first and second potentials for the same

block unit concerned and one of the common opposing signal lines is connected to the opposing signal electrodes provided for the pixels on the same line.

7. (Original) A display device according to claim 3, wherein the display module includes a panel in which the pixels are arranged in a matrix shape, a signal driver, a scan driver and opposing signal driver; signal lines connected to the signal driver; scan lines connected to the scan driver; and opposing signal lines connected to the opposing signal driver; each of the pixels includes a signal electrode, opposing signal electrode and a switch element, the signal electrode is connected to one of the signal lines via the switch element, the opposing signal electrode is connected to one of the opposing signal lines, a first potential is applied to the signal electrodes provided for the pixels on a same line included in a same block unit, a second potential is applied to the opposing signal electrodes provided for the pixels on a same row included in the same block unit, a certain specific pattern is formed by the first and second potentials for the same block unit concerned and one of the common opposing signal lines is connected to the opposing signal electrodes provided for the pixels on the same line.

8. (Original) A display device according to claim 4, wherein the display module includes a panel in which the pixels are arranged in a matrix shape, a signal driver, a scan driver and opposing signal driver; signal lines connected to the signal driver; scan lines connected to the scan driver; and opposing signal lines connected to the opposing signal driver; each of the pixels includes a signal electrode, opposing

signal electrode and a switch element, the signal electrode is connected to one of the signal lines via the switch element, the opposing signal electrode is connected to one of the opposing signal lines, a first potential is applied to the signal electrodes provided for the pixels on a same line included in a same block unit, a second potential is applied to the opposing signal electrodes provided for the pixels on a same row included in the same block unit, a certain specific pattern is formed by the first and second potentials for the same block unit concerned and one of the common opposing signal lines is connected to the opposing signal electrodes provided for the pixels on the same line.

9. (Currently Amended) ~~A display device according to claim 2,~~ A display device comprising a display module which determines a plurality of n (n is an integer equal to or more than 2) pieces of pixels as belonging to one block unit, selects the plurality of pixels in each block unit at the same time and displays a picture image by adding one or a plurality of specific patterns each having different spatial frequencies of each block unit; a display control unit which controls the display module; a picture image signal generation unit which generates picture image signals; and a computing circuit which generates the specific patterns each having different spatial frequencies while weighting the same based on the picture image signals for every block unit;

wherein the display module includes a panel in which the pixels are arranged in a matrix shape, a signal driver, a scan driver and opposing signal driver; signal lines connected to the signal driver; scan lines connected to the scan driver;

opposing signal common lines connected to the opposing signal driver and opposing signal lines connected to the opposing signal common lines; each of the pixels includes a signal electrode, opposing signal electrode and a switch element, the signal electrode is connected to one of the signal lines via the switch element, the opposing signal electrode is connected to one of the opposing signal lines, a first potential is applied to the signal electrodes provided for the pixels on a same line included in a same block unit, a second potential is applied to the opposing signal electrodes provided for the pixels on a same row included in the same block unit, a certain specific pattern is formed by the first and second potentials for the same block unit concerned and one of different opposing signal lines is connected to the opposing signal electrodes provided for the pixels included in a different block unit.

10. (Currently Amended) ~~A display device according to claim 3,~~ A display device comprising a display module which determines a plurality of n (n is an integer equal to or more than 2) pieces of pixels as belonging to one block unit, selects the plurality of pixels in each block unit at the same time and displays a picture image by adding one or a plurality of specific patterns each having different spatial frequencies of each block unit; a display control unit which controls the display module; a picture image signal generation unit which generates picture image signals; and a computing circuit which generates the specific patterns each having different spatial frequencies while weighting the same based on the picture image signals for every block unit;

wherein the computing circuit is a means for generating n pieces of specific patterns each having different spatial frequencies which are weighted based on the picture image signals for every block unit, and the display module is a means for displaying a picture image by adding Np (which is an integer smaller than n) pieces of the specific patterns; and,

wherein the display module includes a panel in which the pixels are arranged in a matrix shape, a signal driver, a scan driver and opposing signal driver; signal lines connected to the signal driver; scan lines connected to the scan driver; opposing signal common lines connected to the opposing signal driver and opposing signal lines connected to the opposing signal common lines; each of the pixels includes a signal electrode, opposing signal electrode and a switch element, the signal electrode is connected to one of the signal lines via the switch element, the opposing signal electrode is connected to one of the opposing signal lines, a first potential is applied to the signal electrodes provided for the pixels on a same line included in a same block unit, a second potential is applied to the opposing signal electrodes provided for the pixels on a same row included in the same block unit, a certain specific pattern is formed by the first and second potentials for the same block unit concerned and one of different opposing signal lines is connected to the opposing signal electrodes provided for the pixels included in a different block unit.

11. (Original) A display device according to claim 4, wherein the display module includes a panel in which the pixels are arranged in a matrix shape, a signal driver, a scan driver and opposing signal driver; signal lines connected to the signal

driver; scan lines connected to the scan driver; opposing signal common lines connected to the opposing signal driver and opposing signal lines connected to the opposing signal common lines; each of the pixels includes a signal electrode, opposing signal electrode and a switch element, the signal electrode is connected to one of the signal lines via the switch element, the opposing signal electrode is connected to one of the opposing signal lines, a first potential is applied to the signal electrodes provided for the pixels on a same line included in a same block unit, a second potential is applied to the opposing signal electrodes provided for the pixels on a same row included in the same block unit, a certain specific pattern is formed by the first and second potentials for the same block unit concerned and one of different opposing signal lines is connected to the opposing signal electrodes provided for the pixels included in a different block unit.

12. (Currently Amended) ~~A display device according to claim 2,~~ A display device comprising a display module which determines a plurality of n (n is an integer equal to or more than 2) pieces of pixels as belonging to one block unit, selects the plurality of pixels in each block unit at the same time and displays a picture image by adding one or a plurality of specific patterns each having different spatial frequencies of each block unit; a display control unit which controls the display module; a picture image signal generation unit which generates picture image signals; and a computing circuit which generates the specific patterns each having different spatial frequencies while weighting the same based on the picture image signals for every block unit;

wherein the display module includes a panel in which the pixels are arranged in a matrix shape, a signal driver, a scan driver and opposing signal driver; signal lines connected to the signal driver; scan lines connected to the scan driver; opposing signal common lines connected to the opposing signal driver and opposing signal lines connected to the opposing signal common lines; each of the pixels includes a signal electrode, opposing signal electrode and a switch element, the signal electrode is connected to one of the signal lines via the switch element, the opposing signal electrode is connected to one of the opposing signal lines, a first potential is applied to the signal electrodes provided for the pixels on a same line included in a same block unit, a second potential is applied to the opposing signal electrodes provided for the pixels on a same row included in the same block unit, a certain specific pattern is formed by the first and second potentials for the same block unit concerned and one of different opposing signal lines. is connected to the opposing signal electrodes provided for the pixels included in a different block unit, and respective different opposing signal lines are connected to the opposing signal electrodes provided for the pixels on different lines included in the same block unit.

13. (Currently Amended) ~~A display device according to claim 3,~~ A display device comprising a display module which determines a plurality of n (n is an integer equal to or more than 2) pieces of pixels as belonging to one block unit, selects the plurality of pixels in each block unit at the same time and displays a picture image by adding one or a plurality of specific patterns each having different spatial frequencies of each block unit; a display control unit which controls the display module; a picture

image signal generation unit which generates picture image signals; and a computing circuit which generates the specific patterns each having different spatial frequencies while weighting the same based on the picture image signals for every block unit;

wherein the computing circuit is a means for generating n pieces of specific patterns each having different spatial frequencies which are weighted based on the picture image signals for every block unit, and the display module is a means for displaying a picture image by adding Np (which is an integer smaller than n) pieces of the specific patterns; and,

wherein the display module includes a panel in which the pixels are arranged in a matrix shape, a signal driver, a scan driver and opposing signal driver; signal lines connected to the signal driver; scan lines connected to the scan driver; opposing signal common lines connected to the opposing signal driver and opposing signal lines connected to the opposing signal common lines; each of the pixels includes a signal electrode, opposing signal electrode and a switch element, the signal electrode is connected to one of the signal lines via the switch element, the opposing signal electrode is connected to one of the opposing signal lines, a first potential is applied to the signal electrodes provided for the pixels on a same line included in a same block unit, a second potential is applied to the opposing signal electrodes provided for the pixels on a same row included in the same block unit, a certain specific pattern is formed by the first and second potentials for the same block unit concerned and one of different opposing signal lines is connected to the opposing signal electrodes provided for the pixels included in a different block unit,

and respective different opposing signal lines are connected to the opposing signal electrodes provided for the pixels on different lines included in the same block unit.

14. (Original) A display device according to claim 4, wherein the display module includes a panel in which the pixels are arranged in a matrix shape, a signal driver, a scan driver and opposing signal driver; signal lines connected to the signal driver; scan lines connected to the scan driver; opposing signal common lines connected to the opposing signal driver and opposing signal lines connected to the opposing signal common lines; each of the pixels includes a signal electrode, opposing signal electrode and a switch element, the signal electrode is connected to one of the signal lines via the switch element, the opposing signal electrode is connected to one of the opposing signal lines, a first potential is applied to the signal electrodes provided for the pixels on a same line included in a same block unit, a second potential is applied to the opposing signal electrodes provided for the pixels on a same row included in the same block unit, a certain specific pattern is formed by the first and second potentials for the same block unit concerned and one of different opposing signal lines is connected to the opposing signal electrodes provided for the pixels included in a different block unit, and respective different opposing signal lines are connected to the opposing signal electrodes provided for the pixels on different lines included in the same block unit.

15. (Currently Amended) A display device according to one of claims 3 through 14 ~~1 through 14~~, wherein the number of pixels in line direction in a block unit is larger than the number of pixels in row direction in the block unit.

16. (Currently Amended) A display device according to one of claims 3 through 14 ~~1 through 14~~, wherein a combination of a plurality of pixels which constitute a block unit is varied.

17. (Currently Amended) A display device according to one of claims 3 through 14 ~~1 through 14~~, wherein the display module is a projection type display, and the projection type display includes a projection pattern display source which displays the specific patterns and a pattern display element, and the pattern display element includes a pair of substrates on which a transparent electrode is formed, a photo conductive layer formed on the transparent electrode and an LC layer sandwiched ~~sandwitched~~ by the pair of substrates.

18. (Currently Amended) A display device according to one of claims 3 through 14 ~~1 through 14~~, wherein the display module is constituted as a means for displaying picture images by sequentially adding the specific patterns.

19. (Currently Amended) A display device according to one of claims 3 through 14 ~~1 through 14~~, wherein the display module is a means for displaying

picture images while computing the specific patterns in the respective pixels and adding the same therein.

20. (Original) A display device according to claim 19, wherein the display module includes a panel in which the pixels are arranged in a matrix shape, a signal driver, a scan driver and a common electrode driver; signal lines connected to the signal driver; scan lines connected to the scan driver; and common electrode lines connected to the common electrode driver, each of the pixels is provided with an adder-subtractor for adding the specific patterns, and the signal lines of which number is equal to the number Np of specific patterns to be added are connected, to the adder-subtractor.

21. (Original) A display device according to claim 20, wherein the panel is an LC panel provided with an LC for the pixels, each of the pixels is provided with capacitance elements of more than Np pieces corresponding to the number of the specific patterns to be added which hold signals sent via the concerned signal lines, and means for coupling the capacitance element concerned and the capacitance of the LC.

22. (Currently Amended) A display device according to one of claims 3 through 14 ~~1 through 14~~, wherein each circuit which constitutes each pixel includes a sample hold means for digital signal and another sample hold means for analogue signals.

23. (Original) A display device according to claim 22, wherein the signal held in the sample hold means for analogue signals is rewritten depending on the signal held in the sample hold means for digital signals to provide a same signal for the pixels included in a same block unit.

24. (Currently Amended) A display device according to one of claims 3 through 14 ~~1 through 14~~, wherein the picture image signal generation unit includes the computing circuit.

25. (Currently Amended) A display device according to one of claims 3 through 14 ~~1 through 14~~, wherein the display control unit includes the computing circuit.

26. (Currently Amended) A display device according to one of claims 3 through 14 ~~1 through 14~~, wherein the display module includes the computing circuit.

27. (Currently Amended) A display device according to one of claims 3 through 14 ~~1 through 14~~, wherein the display module is an LC display module.

28. (Canceled)

29. (New) A display device comprising a display module which determines a plurality of n (n is an integer equal to or more than 2) pieces of pixels as belonging to

one block unit, selects the plurality of pixels in each block unit at the same time and displays a picture image by adding one or a plurality of specific patterns each having different spatial frequencies of each block unit; a display control unit which controls the display module; a picture image signal generation unit which generates picture image signals; and a computing circuit which generates the specific patterns each having different spatial frequencies while weighting the same based on the picture image signals for every block unit;

wherein the display module is a projection type display, and the projection type display includes a projection pattern display source which displays the specific patterns and a pattern display element, and the pattern display element includes a pair of substrates on which a transparent electrode is formed, a photo conductive layer formed on the transparent electrode and an LC layer sandwiched by the pair of substrates.

30. (New) A display device according to claim 29, wherein the computing circuit is a means for generating n pieces of specific patterns each having different spatial frequencies which are weighted based on the picture image signals for every block unit, and the display module is a means for displaying a picture image by adding Np (which is an integer smaller than n) pieces of the specific patterns.

31. (New) A display device comprising a display module which determines a plurality of n (n is an integer equal to or more than 2) pieces of pixels as belonging to one block unit, selects the plurality of pixels in each block unit at the same time and

displays a picture image by adding one or a plurality of specific patterns each having different spatial frequencies of each block unit; a display control unit which controls the display module; a picture image signal generation unit which generates picture image signals; and a computing circuit which generates the specific patterns each having different spatial frequencies while weighting the same based on the picture image signals for every block unit;

wherein each circuit which constitutes each pixel includes a sample hold means for digital signal and another sample hold means for analogue signals.

32. (New) A display device according to claim 31, wherein the computing circuit is a means for generating n pieces of specific patterns each having different spatial frequencies which are weighted based on the picture image signals for every block unit, and the display module is a means for displaying a picture image by adding Np (which is an integer smaller than n) pieces of the specific patterns.